# Embedding computations in tilings (Part 2) 

Andrei Romashchenko
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Set of Wang tiles: a set $\tau \subset C^{4}$
Tiling: a mapping $f: \mathbb{Z}^{2} \rightarrow \tau$ that respects the matching rules

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(ii) all $\tau$-tilings are aperiodic.

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A tile set that simulates itself:


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- $N=$ zoom factor
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- $m(N)=[$ size of the computational zone]:= poly $(\log N)$

A tile set $\tau_{N}$ that simulates itself:


Parameters:

- $N=$ zoom factor (works for all large enough $N$ )
- $k=\#[$ bits in a macro-color] $:=2 \log N+O(1)$
- $m=[$ size of the computational zone $]:=\operatorname{poly}(\log N)$

A tile set $\tau_{N}$ that simulates itself with variable zoom :


- level 1 (macro-tiles): zoom $=N$,
- level 2 (macro-maro-tiles): zoom $=N+1$,
- level 3 (macro-maro-macro-tiles): zoom=N+2,
[Turing machine $\pi$ ] $\mapsto$ tile set $\tau(\pi)$


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$\tau$-tiling exists $\Longleftrightarrow \pi$ never stops

Theorem [Berger 66]. The tiling problem is undecidable (given a tile set we cannot decide algorithmically whether it can tile the plane).
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$\omega=\omega_{0} \omega_{1} \ldots \omega_{n} \ldots$
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$N$-macro-colors include the prefix $\omega_{[0: \log N]}$
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Definition. $\omega=\omega_{0} \omega_{1} \ldots \omega_{n} \ldots$ is a separator if

- $\omega_{n}=0$ for every $n$ s.t. the $n$-th Turing machine $(n)=0$,
- $\omega_{n}=1$ for every $n$ s.t. the $n$-th Turing machine $(n)=1$.

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## Proof:

- embed an $\omega$ in our tiling
- useful computation: simulate in parallel $n$-th TM(n) and check that the embedded $\omega$ is a separator
- every (infinite) tiling must include an incomputable $\omega$

